Google	<u>Web</u>	Images	<u>Video</u>	<u>News</u>	Maps	more »
	weight register context thread switch					

2003 Search

Ad Sc Sc

Scholar All articles - Recent articles Results 1 - 10 of about 18,400 for weight register context thre

Adaptive load migration systems for PVM - ▶ pdx.edu pon

J Casas, R Konuru, SW Otto, R Prouty, J Walpole - Proceedings of the 1994 conference on Supercomputing table ..., 1994 - portal.acm.org

... cesses (ULPS) and can be thought of as light-weight, Unix-like processes that are independently ... A ULP, like a thread, defines a register context and ...

Cited by 84 - Related articles - Web Search - All 19 versions

A user-level process package for PVM

R Konuru, J Casas, R Prouty, S Ótto, J Walpole - Scalable High-Performance Computing Conference, 1994. ..., 1994 - ieeexplore.ieee.org

... C PVM supports light-weight vartual processors that ... by simply saving its current register context and restoring ... the cost of higher context switch over- heads ...

Cited by 39 - Related articles - Web Search - All 7 versions

[PDF] ▶ A library implementation of POSIX threads under UNIX

F Mueller - Proceedings of the USENIX Conference, 1993 - moss.csc.ncsu.edu

... A thread's light weight should reduce the context ... changed dur- ing a thread's context switch are those ... local state contained in the register windows (ins ...

Cited by 179 - Related articles - View as HTML - Web Search - All 18 versions

The design and implementation of a context switching FPGA

SM Scalera, JR Vazquez, LM Co, H Sanders - FPGAs for Custom Computing Machines, 1998. Proceedings. IEEE ..., 1998 - leeexplore.leee.org

... In this manner, a reduction in size, weight, and power can ... A or B). Upon becoming active, a context can either ... it's previous data or load a public register. ...

Cited by 100 - Related articles - Web Search - All 3 versions

Fast context switches: compiler and architectural support for preemptive scheduling -

★ fsu.edu (PS)

JS Snyder, DB Whalley, TP Baker - Microprocessors and Microsystems, 1995 - Elsevier ... When a context switch occurs, only the state of the ... which contains only pointers

into the register bank, needs ... in the environment of a heavy- weight process. ...

Cited by 18 - Related articles - Web Search - All 7 versions

Apparatus and method for interrupt handling in a multi-threaded operating system kernel

SR Kleiman... - US Patent 5,515,538, 1996 - freepatentsonline.com

... 80: threads 40-47, and Light Weight Processes(LWPs ... Having made the initial register and pointer settings ... having to make a complete context switch, Thread B looks ...

Cited by 59 - Related articles - Web Search - All 3 versions

On the design of Chant: a talking threads package - Imaga, gov independent of the design of Chant: a talking threads package - Imaga, gov independent of the design of Chant: a talking threads package - Imaga, gov independent of the design of Chant: a talking threads package - Imaga, gov independent of the design of Chant: a talking threads package - Imaga, gov independent of the design of Chant: a talking threads package - Imaga, gov independent of the design of the design

M Haines, D Cronk, P Mehrotra - Supercomputing'94. Proceedings, 1994 - leeexplore.ieee.org

... Threads are often classified by their "weight", which corresponds to ... The context of a Unix process includes the hardware register, kernel stack ...

Cited by 129 - Related articles - Web Search - Library Search - All 9 versions

The named-state register file: Implementation and performance - > stanford.edu (PS)

PR Nuth, WJ Dally - First Annual International Symposium on High-Performance ..., 1995 doi.ieeecomputersociety.org

... of the NSF on register usage, register reload traffic, and ... are often composed of concurrent light-weight threads in a ... 24,13,1,25] reduce context switch time by ...

Cited by 12 - Related articles - Web Search - All 14 versions

System and method for managing variable weight thread contexts in a multithreaded computer system

RK Manikundalam, FL Rawson III - US Patent 5,799,188, 1998 - freepatentsonline.com ... A thread is a light-weight process that has ... For example, a floating point register state could ... A context switch occurs whenever a thread must give execution ... Cited by 6 - Related articles - Web Search - All 3 versions

MiThOS-a real-time micro-kernel threads operating system - wuniv-lehavre.fr. Por F Mueller, V Rustagi, TP Baker - Real-Time Systems Symposium, 1995. Proceedings., 16th IEEE, 1995 leeexplore.leee.org

... program frames exceeds the number of register windows ... at the kernel level for light-weight processes [14 ... code size and includes a fast context switch algo- rithm ...

Cited by 20 - Related articles - Web Search - All 12 versions

Key authors: F Mueller - H Massalin - M Haines - M Rozier - J Casas

Goooooooogle ▶

1 2 3 4 5 6 7 8 9 10 Result Page: Next

> weight register context thread switch Search

Google Home - About Google - About Google Scholar

©2008 Google

http://scholar.google.com/scholar?as_q=weight+register+context+thread+switch&num=10&bt... 1/2/09